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3. Observe The Output Of The AF Generator Using CRO; It Should Be A Sine Wave Of 200Hz Frequency With 3Vpp Amplitude. 4. Verify The Output Of The DC Source With Multi-meter / Scope, Output Should Vary 0 To +5v. 5. Observe The Output Of The Clock Generator Using CRO, They Should Be 64 KHz And 8KHz Frequency Of Square Wave With 5Vpp Amplitude. 6. Jan 4th, 2021

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The Flow Of The Signal Processing Steps Shown In Fig. 1 Represents A Typical Arrangement; However, The Blocks Are Sometimes Implemented In A Different Order. For Example, Multiplexing Can Take Place Prior To Channel Encoding, Prior To Modulation, Or-with A Two-step Modulation Process Apr 8th, 2021

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